THE COMMISSIONER OF PATENTS AND TRADEMARKS WASHINGTON, D.C. 20231

Sir:



Transmitted herewith for filing is the Utility patent application of:

INVENTOR: TETSUYA FUKUOKA, TAKESHI MIYAZAKI AND KATSUICHI TOMOBE

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT

Enclosed are:

- ⊠ 8 sheets of □ formal □ informal drawings.
- ☑ Transmittal of Japanese patent application 2002-216258 to claim priority date of July 25, 2002.
- ⋈ An Executed Declaration.
- ☑ An assignment executed by inventor to Hitachi, Ltd.
- ☑ The filing fee is calculated as shown below:

For	Numbe	R FILED	Number Extra	RATE	CALCULATIONS
TOTAL CLAIMS	14	-20	0	x\$18 =	0.00
INDEPENDENT CLAIMS	4	-3	1	x\$84 =	84.00
MULTIPLE DEPENDENT CLAIM(S) (if applicable) +\$280 =					
			BASIC FEE		\$750.00
TOTAL OF ABOVE				834.00	
			37 C.F.R. §§ 1.9, 1.27, 1.28).	
IF APPLICABLE, VERIFIED	SIATEMENT	AUST BE ATTAC	неи	70	
				TOTAL	834.00

=	Please charge my Deposit Account Number	in the amount of	to cover the filing and assignment recordation
	fees. A duplicate copy of this paper is enclosed.		<u> </u>

- A check in the amount of \$874.00 to cover the filing fee and assignment fee is enclosed.
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to **Deposit Account Number 08-1480**.

Respectfully submitted,

Stanley P. Fisher

Registration Number 24.34

Juan Carlos A Marquez
Registration Number 34,872

REED SMITH LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 July 16, 2003 10/619263 10/619263